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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,571	11/19/2001	Manfred Bartz	CYPR-CD01168M	1215

7590 05/23/2005

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EXAMINER
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DIMYAN, MAGID Y

ART UNIT	PAPER NUMBER
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2825

DATE MAILED: 05/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

N.A

<b>Office Action Summary</b>	<b>Application No.</b> 09/989,571	<b>Applicant(s)</b> BARTZ ET AL.	
	<b>Examiner</b> Magid Y. Dimyan	<b>Art Unit</b> 2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 21 April 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 16-36 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 21-27 and 30-36 is/are rejected.
- 7) ☒ Claim(s) 16-20, 28 and 29 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 18 October 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 21 April 2005 has been entered. It is acknowledged that claims 1 – 15 have been cancelled without prejudice, claim 16 has been amended, and claims 21 – 36 have been added without adding new matter.

### ***Claim Objections***

2. Claims 16 - 20 are objected to because of the following informalities:
- Claim 16, line 7, delete "and said" and insert --said--.
  - Claim 16, line 9, insert --layout of-- after "characteristics of said".
  - Claim 18, line 9, item e3). It is not clear what is being repeated.
3. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 21 – 27 and 30 - 36 are rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 6,817,005 to Mason et al. (hereinafter, "Mason").

6. Regarding claim 21, Mason cites a method of designing a circuit for a programmable device (see Abstract; col. 1, lines 9 – 11) comprising: (a) selecting a module to be used in the circuit (see Fig. 1C; col. 10, lines 42 – 65); (b) requesting a valid placement for the module in a GUI comprising resource images representing programmable resources, wherein the valid position specifying at least one of the resource images (see Figs. 1D and 1E; col. 2, line 58 – col. 3, line 10); and (c) selecting valid placement to place module in the GUI (see also col. 7, lines 1 – 57). Thus, Mason teaches all the claimed elements.

7. As per claim 22, see col. 5, lines 47 – 51, which show how a Delay Locked Loop (i.e., a programmable analog block) can be included in the programmable device, as claimed.

8. As for claim 23, see (4) above, as well as col. 1, lines 13 – 35, which disclose the claimed limitation of the programmable digital blocks included in the resources used in the design.

9. Referring to claims 24 and 25, see also col. 5, lines 57 – 63; col. 8, line 45 – col. 9, line 30, which recite how additional modules can be placed in the design via a GUI as claimed.

10. Pursuant to claim 26, see col. 1, line 65 – col. 2, line 15; col. 5, line 45 – 48; col. 24, line 34 – 47, which show how the claimed element pertaining to the interconnectivity between resources are configured using a GUI.

11. Regarding claim 27, see (4) above; as well as col. 2, line 58 – col. 3, line 47, which cite how the input/output pins can be configured in a programmable device, as claimed.

12. As for claim 30, Mason discloses a method of using a GUI to facilitate implementing a design in a programmable device (see (4) above) comprising: (a)

selecting a module for placement in a GUI and requesting valid placements for the module in a GUI (see (4) above); (b) receiving indications of valid placements of the module in the GUI (see also col. 7, lines 1 – 14); and (c) selecting one of valid placements to place module in a GUI (see (4) above). Mason discloses all the claimed limitations.

13. Claims 31 and 32 contain the same elements as claims 22 and 23, respectively, and thus the same rejections apply.

14. Pursuant to claim 33, see col. 5, lines 40 – 48, which teach how a GUI is used to enter design constraints, including timing, specification and mapping of configurable logic blocks, as claimed.

15. As for claim 34, see Fig. 8A, block 803; col. 10, lines 15 – 29; col. 17, lines 35 – 55, which all cite the claimed element of the parameter mapping to a register address of a programmable.

16. Claims 35 and 36 contain the same elements found in claims 27 and 26, respectively, and therefore the same rejections also apply.

### ***Allowable Subject Matter***

17. Claims 16 – 20 are allowed.

18. Claims 28 and 29 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

19. The following is a statement of reasons for the indication of allowable subject matter: The allowed claims 16 – 20 contain elements pertaining to the placement of modules using a GUI in a programmable device wherein user modules are overlaying resource images. Prior art does not teach this limitation. The objected claims 28 and 29 include elements regarding ways of selecting module parameters, and creating a source code program using an API that are disclosed in the references of record.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Magid Y. Dimyan whose telephone number is (571) 272-1889. The examiner can normally be reached on Monday - Friday 8:00 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

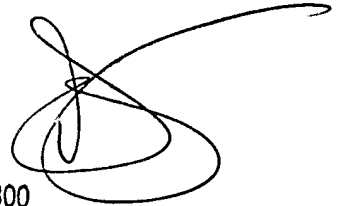
Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Magid Y Dimyan  
Examiner  
Art Unit 2825

myd  
13 May 2005

H-1D

A. M. Thompson  
Primary Examiner  
Technology Center 2800

A handwritten signature in black ink, consisting of a large, stylized loop with a horizontal line extending to the right.